

# 68K Programmer's Reference Manual Errata

by: Microcontroller Division

This errata document describes corrections to the *68K Programmer's Reference Manual*, order number M68000PRM. Please check our website at <http://www.freescale.com/> for the latest updates.

The current available version of the *68K Programmer's Reference Manual* is Revision 1.

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# 1 Errata for Revision 1

**Table 1. M68000 RM Rev 1 Errata**

Location	Description
Page 5-35	In the Instruction Fields section of the FDBcc instruction summary, the displacement field description should be: "Specifies the branch distance (from the address of the instruction plus four) to the destination in bytes."
Page 5-71	In the Instruction Format figure of the FMOD instruction summary, bits 6:0 of the second word should be 0100001 (matching the figure on page 8-33), not 0101101.
Page 5-78	In the Instruction Format figure of the FMOVE instruction summary: <ul style="list-style-type: none"> <li>• The COPROCESSOR ID field should be located in bits 9-11</li> <li>• Bit 12 has a fixed value of 1</li> </ul>
Page 6-12	In the FRESTORE instruction summary, the description for the BUSY state frame should begin with "This state frame is 100 (\$64) bytes long in the MC68040..."

## 2 Revision History

Table 2 provides a revision history for this document.

**Table 2. Revision History**

Rev. Number	Substantive Changes	Date of Release
0.1	Initial release as text document.	07/2005
1	<ul style="list-style-type: none"> <li>• Converted errata from text document to standard Freescale format</li> <li>• Added FMOD instruction errata to <a href="#">Table 1</a></li> <li>• Light edit for grammar, punctuation, and format</li> </ul>	03/2007

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